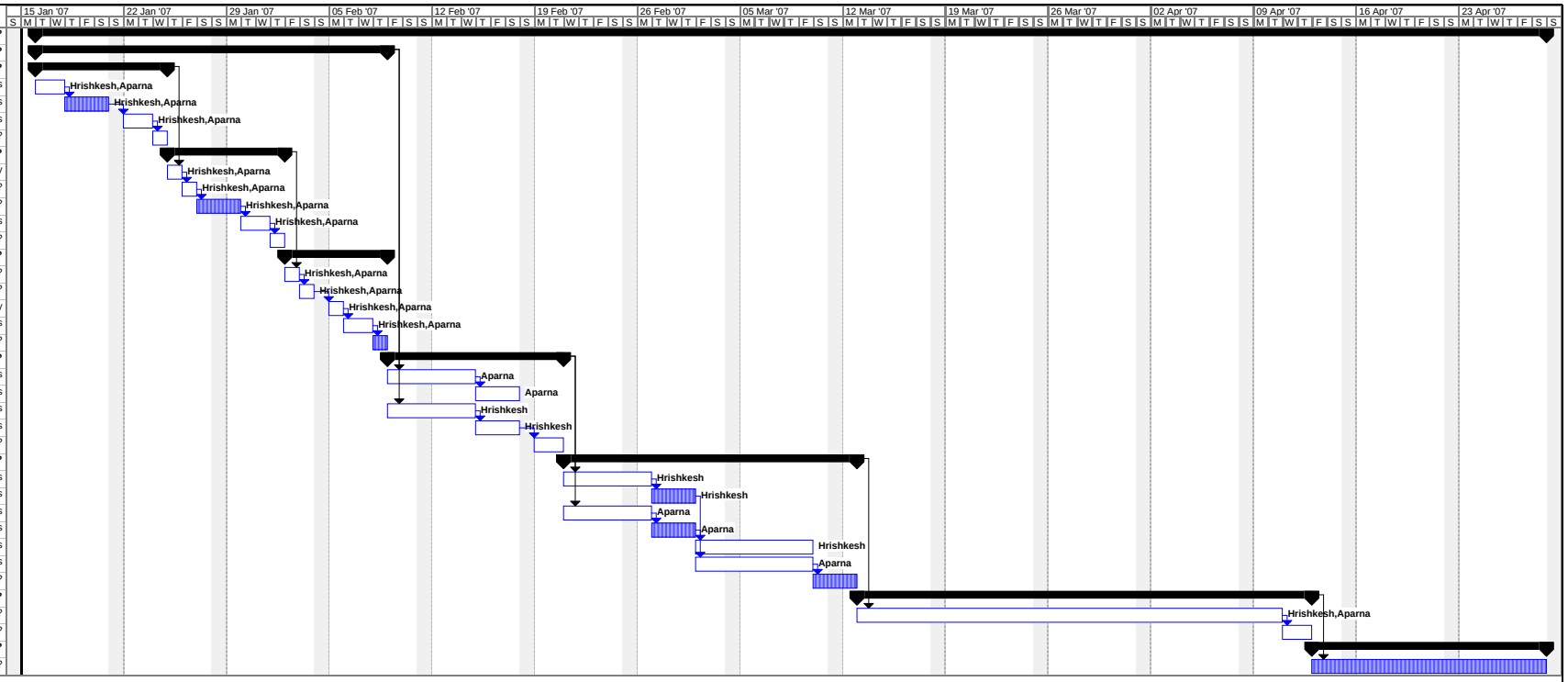
ID	Task Name	Duration
1	**Video Generator**	**89 days?**
2	**Phase I - Study**	**21 days?**
3	**FPGA**	**8 days?**
4	What are FPGA's	2 days
5	How are FPGA's programm	3 days
6	XESS/Digilent FPGA Board	2 days
7	Documentation	1 day?
8	**Video Signals**	**7 days?**
9	What is video?	1 day
10	Raster Scanning	1 day?
11	Concepts of RGB Signals	2 days?
12	Importance of HS and VS	2 days
13	Documentation	1 day?
14	**Xilinx ISE Webpack**	**6 days?**
15	Get familiar with Xilinx Tools	1 day?
16	Understand the concepts of	1 day?
17	Understand the concept of	1 day
18	Dummy code download on	2 days
19	Documentation	1 day?
20	**Phase II - Generate RGB**	**10 days?**
21	Verilog Code to generate HS and	5 days
22	Testbench to test HS and VS	3 days
23	Verilog Code to generate RGB	5 days
24	Testbench to test RGB generatic	3 days
25	Phase II Documentation	2 days?
26	**Phase III - Generate patterns**	**17 days?**
27	Verilog code to generate a circle	5 days
28	Testbench to test the above	3 days
29	Verilog code to generate Squar	5 days
30	Testbench	3 days
31	Generate pattern given the coord	7 days
32	Testbench	7 days
33	Phase III Documentation	2 days?
34	**Phase IV**	**27 days?**
35	Human Interface<TBD>	25 days?
36	Phase IV Documentation	2 days?
37	**Documentation**	**14 days?**
38	Final report	14 days?



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